



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of: **TAKESHITA, Mitsunori, et al.**

Group Art Unit: 1792

Serial No.: 10/593,282

Examiner: **FORD, Nathan K.**

Filed: **September 18, 2006**

P.T.O. Confirmation No.: 8499

**FOR: SUBSTRATE PROCESSING APPARATUS AND MANUFACTURING METHOD
FOR A SEMICONDUCTOR DEVICE**

REQUEST FOR STATUS

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Date: June 18, 2009

Sir:

The undersigned attorney requests the status of the above-identified application.

The application was filed on **September 18, 2006** in the U.S. Patent and Trademark
Office and to date no Office Action has been received.

In the event any fees are required in connection with this paper, please charge Deposit
Account No. 01-2340.

Respectfully submitted,

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